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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/733,302	12/12/2003	Junichi Tamura	OKI 402	7322
23995	7590	03/24/2006	EXAMINER	
RABIN & Berdo, PC 1101 14TH STREET, NW SUITE 500 WASHINGTON, DC 20005			BARTON, JONATHAN A	
			ART UNIT	PAPER NUMBER
			2186	

DATE MAILED: 03/24/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/733,302	Applicant(s) TAMURA, JUNICHI	
	Examiner Jonathan Barton	Art Unit 2186	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 12/12/2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-14 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-14 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 12 December 2003 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>12/12/2003</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Drawings

1. Figures 8 and 9 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claim*** rejected under 35 U.S.C. 102(e) as being anticipated by Ng et al. (US 2003/0202270).

a. As for claim 1 Ng discloses

- i. a) storing data in a first data storage section (Par. 49 Lines 3-6);
 - ii. b) storing data rearrangement information in a stack (Par. 44 and 43); and
 - iii. c) reading the data stored in the first data storage section, and storing the data in a second data storage section based on the data rearrangement information stored in the stack (Par. 43, 49 Lines 4-9).
- b. As for claim 11 Ng discloses
 - iv. The reading and the storing are carried out by using an address conversion table and a corresponding stack pointer (Par. 44, 47 Lines 2-3).
- c. As for claim 12 Ng discloses
 - v. calculating logic OR operation or logic ADD operation of a read address and an offset register (Par. 52).
- d. As for claim 13 Ng discloses
 - vi. the reading and the storing are carried out by using a register substituted for the stack pointer (Par. 43-44).
- e. As for claim 14 Ng discloses
 - vii. the data stored in the address conversion table includes byte write information (Par. 35).
- f. As for claim 4 Ng discloses
 - viii. a) storing a plurality of data in a first data storage section (Par. 49 Lines 3-6);

- ix. b) storing data rearrangement information in a stack (Par. 44 and 43); and
 - x. c) reading the plurality of data stored in the first data storage section in an order based on the data rearrangement information stored in the stack, and storing the data in a second data storage section (Par. 43, 49 Lines 4-9).
- g. As for claim 8 Ng discloses
- xi. a) storing a plurality of data in a first data storage section (Par. 49 Lines 3-6);
 - xii. b) storing data rearrangement information in a stack (Par. 44 and 43); and
 - xiii. c) reading the plurality of data stored in the first data storage section, and storing the data in a second data storage section based on the data rearrangement information stored in the stack (Par. 43, 49 Lines 4-9).

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

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5. Claims 2, 3, 5, 6, 7, 9 and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ng et al. (US 2003/0202270) in view of Jhung (US 6,304,847).

h. 2, 5 and 9 Ng fails to disclose the following limitation, which is taught by Jhung:

xiv. the data rearrangement information contains an address of the second data storage section (Col. 10 Lines 51-54).

xv. It would have been obvious to one of ordinary skill in the art at the time of the invention to have combined the address data taught by Jhung with the data rearrangement system/method discloses Ng because both systems rearrange data in an effort to increase the speed and efficiency of a system and using the address data taught by Jhung provides a method of reordering that is not software intensive and doesn't waste valuable resources.

i. As for claims 3, 6 and 10 Jhung teaches

xvi. the first data storage section is a register; and the second data storage section is a random access memory (Col. 10 Lines 54-58, Col. 13 Lines 29-52).

j. As for claim 7 Jhung teaches

xvii. the first data storage section and the second data storage section are random access memories (Col. 10 Lines 54-58).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jonathan Barton whose telephone number is 571-272-8157. The examiner can normally be reached on Monday - Friday 8:30 - 5:00.


If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matt Kim can be reached on 571-272-4182. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jonathan Barton
Examiner
Art Unit 2186



JB



MATTHEW KIM
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100